## General Program

### 2017 October 18, Wednesday

**08:00 – 09:00**  
Check in/ Registration  
(Zyxel Auditorium, National Tsing Hua University)

**09:00 – 10:15**  
Opening Ceremony and Keynote Lecture  
Speaker: Dr. Simon M. Sze  
Title: The Transistor and The Floating-Gate Memory – On the Birthplace of Semiconductor Technology – Bell Laboratories  
Chairs: Dr. Juin J. Liou and Dr. Chih-Wen Lu  
(Zyxel Auditorium, National Tsing Hua University)

**10:15 – 10:30**  
Coffee Break

**10:30 – 12:30**  
Keynote Lectures  
Speaker: Dr. Nicky Lu  
Title: A New Silicon Age 4.0: Generating Semiconductor-Intelligence Paradigm with a Virtual Moore's Law Economics and Heterogeneous Technologies  
Speaker: Dr. Gene Sheu  
Title: High Voltage BCD Device Design and Simulation  
(Zyxel Auditorium, National Tsing Hua University)

**12:30 – 14:00**  
Lunch Break  
(Macronix Learning Resource Center, National Tsing Hua University)

**14:00 – 15:30**  
Session W1A  
Electron Devices and Materials  
(Room A, 3F)  
Session W1B  
Electron Devices and Materials  
(Room B, 3F)  
Session W1C  
Optoelectronic materials, devices and systems  
(Room C, 3F)  
Session W1D  
Solid – State Circuits and Systems  
(Room D, 3F)

**15:30 – 16:00**  
Coffee Break

**16:00 – 17:30**  
Session W2A  
Front – End of Line (FEOL) / Middle – of – Line (MOL) / Back – End of Line (BEOL) Reliability  
(Room A, 3F)  
Session W2B  
Electron Devices and Materials  
(Room B, 3F)  
Session W2C  
Optoelectronic materials, devices and systems  
(Room C, 3F)  
Session W2D  
Solid – State Circuits and Systems  
(Room D, 3F)

**18:00 – 19:30**  
Welcome Reception  
(1F, Macronix Learning Resource Center, National Tsing Hua University)
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| 08:00 – 08:30 | Check in/ Registration  
(Zyxel Auditorium, National Tsing Hua University) |
| 08:30 – 10:30 | Keynote Lectures  
Speaker: Dr. Tim Kwang Ting Cheng  
Title: ReRAM: Devices, Integration, Architecture, and Applications  
Speaker: Dr. John M. Dallesasse  
Title: Enhancing Silicon: Progress on Silicon Photonics and Heterogeneous Integration  
Chairs: Dr. Jyuo-Min Shyu and Dr. Charles Surya  
(Zyxel Auditorium, National Tsing Hua University) |
| 10:30 – 11:00 | Coffee Break  
(Macronix Learning Resource Center, National Tsing Hua University) |
| 11:00 – 12:30 | Session T1A  
Solid – State Circuits and Systems  
(Room A, 3F)  
Session T1B  
(Room B, 3F)  
Session T1C  
Electron Devices and Materials  
(Room C, 3F)  
Session T1D  
Solid – State Circuits and Systems  
(Room D, 3F) |
| 12:30 – 14:00 | Lunch Break  
(Macronix Learning Resource Center, National Tsing Hua University) |
| 14:00 – 15:30 | Session T2A  
Solid – State Circuits and Systems  
(Room A, 3F)  
Session T2B  
Solid – State Circuits and Systems  
(Room B, 3F)  
Session T2C  
Optoelectronic materials and devices & Magnetic material and circuit  
(Room C, 3F)  
Session T2D  
Nanotechnology and Sensor  
(Room D, 3F)  
Poster  
Session TP  
Solid-State Circuits and Systems, EDA  
(Exhibition, 1F) |
| 15:30 – 16:00 | Coffee Break |
| 16:00 – 17:30 | Session T3A  
Electron Devices and Materials  
(Room A, 3F)  
Session T3B  
Electron Devices and Materials  
(Room B, 3F)  
Session T3C  
Integration & Sensor  
(Room C, 3F)  
Session T3D  
Solid – State Circuits and Systems  
(Room D, 3F) |
| 18:00 – 21:00 | Banquet  
(Pengyuan Agora Garden) |
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<td>Special Focus Session – 3D Integration</td>
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Look forward to seeing you in EDSSC 2018